

CUSTOMER NO.: 24498
Serial No. 10/626,045

Reply to Office Action dated: 04/04/06
Response dated: 05/19/06

PATENT
PD020088

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Amendments to the claims

Please cancel claim 4 without prejudice.

Please amend claims 1, 6 and 8 as follows:

1. (Currently amended) Method for testing video-technological devices, characterized by generating a test signal in which the hue and the colour saturation are periodically altered, and by forming a luminance signal by a sinusoidal oscillation whose amplitude rises and on which a DC component is superposed.
2. (Original) Method according to Claim 1, characterized by altering the colour saturation more slowly than the hue, so that a colour circle with an increasing diameter is generated.
3. (Original) Method according to Claim 1, characterized by forming colour value signals by sinusoidal oscillations which are phase-shifted by 120° with respect to one another, whose amplitudes rise and on which a DC component is superposed.
4. (Cancelled)
5. (Original) Method according to Claim 3, characterized by linearly rising the amplitudes.
6. (Currently amended) Method according to Claim 1 [[4]], characterized by linearly rising the amplitudes.
7. (Original) Method according to Claim 3, characterized by periodically repeating the amplitude rise at the line frequency.
8. (Currently amended) Method according to Claim 1 [[4]], characterized by periodically repeating the amplitude rise at the line frequency.

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9. (Original) Method according to Claim 5, characterized by periodically repeating the amplitude rise at the line frequency.
10. (Original) Method according to Claim 6, characterized by periodically repeating the amplitude rise at the line frequency.
11. (Original) Arrangement for generating a test signal for testing video-technological devices, characterized in that colour value signals are stored in a memory, which signals are formed by sinusoidal oscillations which are phase-shifted by 120° with respect to one another, whose amplitudes rise and on which a DC component is superposed, and in that, for the read-out of the stored colour value signals a pixel counter is connected to address inputs of the memory.
12. (Original) Arrangement according to Claim 11, characterized in that a luminance signal is stored in a memory, which signal is formed by a sinusoidal oscillation whose amplitude rises and on which a DC component is superposed, and in that, for the read-out of the stored luminance signal, a pixel counter is connected to address inputs of the memory.
13. (Original) Arrangement according to Claim 11, characterized in that the amplitudes rise linearly.
14. (Original) Arrangement according to Claim 12, characterized in that the amplitudes rise linearly.
15. (Original) Arrangement according to Claim 11, characterized in that the amplitude rise is repeated periodically at the line frequency.
16. (Original) Arrangement according to Claim 12, characterized in that the amplitude rise is repeated periodically at the line frequency.
17. (Original) Arrangement according to Claim 13, characterized in that the

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amplitude rise is repeated periodically at the line frequency.

18. (Original) Arrangement according to Claims 14, characterized in that the amplitude rise is repeated periodically at the line frequency.